

HPI Kolloquium

16.11.2010, 10:00 Uhr

Hasso-Plattner-Institut, Vorlesungsgebäude, Auditorium 1
Prof.-Dr. Helmert-Str. 2-3, 14482 Potsdam-Babelsberg

Prof. John L. Hennessy
Stanford University

„PHONES TO WAREHOUSES“

Kurz-CV

John L. Hennessy joined Stanford's faculty in 1977 as an assistant professor of electrical engineering. He rose through the academic ranks to full professorship in 1986 and was the inaugural Willard R. and Inez Kerr Bell Professor of Electrical Engineering and Computer Science from 1987 to 2004.

From 1983 to 1993, Dr. Hennessy was director of the Computer Systems Laboratory by the Departments of Electrical Engineering and Computer Science. He served as chair of computer science from 1994 to 1996 and, in 1996, was named dean of the School of Engineering. In 1999, he was named provost, the university's chief academic and financial officer. In October 2000, he was inaugurated as Stanford University's 10th president.

A pioneer in computer architecture, in 1981 Dr. Hennessy drew together researchers to focus on a computer architecture known as RISC (Reduced Instruction Set Computer), a technology that has revolutionized the computer industry by increasing performance while reducing costs. In addition to his role in the basic research, Dr. Hennessy helped transfer this technology to industry. In 1984, he cofounded MIPS Computer Systems, now MIPS Technologies, which designs microprocessors. In recent years, his research has focused on the architecture of high-performance computers.

Dr. Hennessy is a recipient of the 2000 IEEE John von Neumann Medal, the 2000 ASEE Benjamin Garver Lamme Award, the 2001 ACM Eckert-Mauchly Award, the 2001 Seymour Cray Computer Engineering Award, a 2004 NEC C&C Prize for lifetime achievement in computer science and engineering and a 2005 Founders Award from the American Academy of Arts and Sciences. He is a member of the National Academy of Engineering and the National Academy of Sciences, and he is a fellow of the American Academy of Arts and Sciences, the Association for Computing Machinery, and the Institute of Electrical and Electronics Engineers.

Gastgeber: Prof. Dr. Christoph Meinel